AMENDMENTS TO THE SPECIFICATION:

Please amend the specification as follows:

Please amend paragraph 0006 on page 2 as follows:

[0006]   This non-uniform access results in a disadvantage in NUMA systems in that a latency is introduced each time a processor accesses shared memory, depending on the combination of CPUs and nodes upon which a job is scheduled to run. In particular, it is possible for program pages to reside "far" from the processing data, resulting in a decrease in the efficiency of the system by increasing the latency time required to obtain this data. Furthermore, this latency is unpredictable because [[is]] it depends on the location where the shared memory segments for a particular program may reside in relation to the CPUs executing the program. This affects performance prediction, which is an important aspect of parallel programming. Therefore, without knowledge of the topology, performance problems can be encountered in NUMA machines.

Please amend paragraph 0014 bridging pages 5 and 6 as follows:

[0014]   In another aspect, the present invention resides in a [[a]] computer system comprising including resources physically located in more than one module, [[said]] the resources including a plurality of processors being interconnected by a number of interconnections in a physical topology providing non-uniform access to other resources of [[said]] the computer system, a method of scheduling a job to [[said]] the resources, [[said]] the method comprising including the steps of:

(a)   periodically assessing a status of the resources and sending status information signals indicative of the status of the resources to a job scheduling unit;

(b)   assessing, at the job scheduling unit, the resources required to execute a job;

(c)   comparing, at the job scheduling unit, the resources required to execute the job and resources available based on the status information signals; and

(d)   scheduling the job to the resources which are available to execute the job as based on the status information signals and the physical topology, and the resources required to execute the job.